

CPU Communications Port Setup Parameters (Communication Protocols Only)

WORD 1	RTS On-delay	Communication Timeout (% of timeout)	Protocol Mode	RTS Off-delay
Oyyy Ottt mmmm mxxx	yyy	ttt	mmmmm	xxx
450: V772 350: V7655 250, 260: V7655 05, 06: V7655	000 = 0mS 001 = 2mS 010 = 5mS 011 = 10mS 100 = 20mS 101 = 50mS 110 = 100mS 111 = 500mS	000 = 100% 001 = 120% 010 = 150% 011 = 200% 100 = 500% 101 = 1000% 110 = 2000% 111 = 5000%	10000 = K-Sequence 01000 = DirectNet 00100 = Modbus	000 = 0mS 001 = 2mS 010 = 5mS 011 = 10mS 100 = 20mS 101 = 50mS 110 = 100mS 111 = 500mS

Word 2	Parity	StopBits	Echo Suppression	Baud Rate	Protocol Mode (DirectNet)	Secondary Address
K-Sequence, DirectNet, & Modbus pps0 ebbb xaaa aaaa	pp	s	e Valid for 06 and 260 only	bbb	x	xaaaaaaaa (DirectNet) _aaaaaaaa (K-Seq & Modbus)
450: V773 350: V7656 250, 260: V7656 05, 06: V7656	00 = None 10 = Odd 11 = Even	0 = 1 bit 1 = 2 bits	0 = 232 or 422 1 = 485 2 wire	000 = 300 001 = 600 010 = 1200 011 = 2400 100 = 4800 101 = 9600 110 = 19200 111 = 38400	0 = Hex 1 = ASCII	K-Sequence: 1-90 DirectNet: 1-90 Modbus: 1-247

Word 3	
450: V767 350: V7657 250, 260: V7657 05, 06: V7657	The CPU will accept the setup values when a 'setup complete' flag is written to this V-memory location. For the 450 this flag is a value of K5555, representing the 4 available ports, and a value of K0500 for the 05/06, 250 and 350 CPUs. When the CPU checks this value, it will change the '5' to an 'A' if the port settings have been accepted, or an 'E' if there was an error.